

ABSTRACT OF THE DISCLOSURE

A temperature independent CMOS reference voltage
5 circuit includes a CMOS current mirror circuit containing
first and second CMOS transistors of a first polarity. A
temperature compensation circuit is coupled to the CMOS
current mirror circuit, and contains a first resistor, a
second resistor, and third and fourth CMOS transistors of a
10 second polarity. The third and fourth CMOS transistors are
configured to operate substantially in a subthreshold
region. One of the third and fourth CMOS transistors is
diode connected.

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